

## Registration Form

Name of the Applicant (first, last):

.....  
.....

Father's Name:.....

Mother's Name:.....

Gender:.....

Date of Birth:.....

Designation: .....

Highest Qualification: .....

Name and Address of the Organization/Institute:.....

Postal Address:.....

.....

Category : (GEN/OBC/SC/ST/Others).....

City/Town:.....

Email:.....

Phone Number:.....

Mobile Number:.....

Do you need accommodation?

(Yes/No):.....

Transaction ID (Applicable for Online

Transaction):.....

Signature of the Applicant:.....

Signature and Seal of the Forwarding Authority

Name

.....

Designation

.....

Note:

i) The Faculty/Staff are requested to submit the NOC from respective

department before attending the session.

ii) The Faculty member belonging to SC/ST Category need to carry caste

Certificate(both in original & photocopy).

iii) The participant need to carry the Institute/Organization valid Identity

Card (Both in Original & Photocopy).

iv) Accommodation for outstationed participants can be arranged at

MITWPU Guest House, Kothrud Campus. The price details are:

• AC Room - Rs.700 per person per day

• Non AC- Rs.400 per person per day

For Accommodation queries, please contact: Rajkumar D. Komati,

Cell: +91 9822292281, Email: rajkumar.komati@mitcoe.edu.in

Manisha R. Ingle, Cell: +91 9960894948,

Email: [manisha.ingle@mitpune.edu.in](mailto:manisha.ingle@mitpune.edu.in)

Affix passport  
size  
photograph

## Course Coordinators from Academy

- **Prof. Ratnajit Bhattacharjee**  
Principal Investigator, E&ICT Academy, IIT Guwahati.
- **Dr. Gaurav Trivedi**  
Co-Principal Investigator, E&ICT Academy, IIT Guwahati.

## Experts

The experts are from Semiconductor Laboratory (SCL), Chandigarh which is an indigenous IC fabrication facility developed by Department of Space, Government of India..

## Committee Members from MIT-WPU

### ADVISORY BOARD

- **Dr. Inder K. Bhat**  
Vice-Chancellor, MIT-World Peace University, Pune.
- **(Gp Capt) D. P. Apte**  
Registrar, MIT-World Peace University, Pune.
- **Dr. Shrihari Honwad**  
Provost, MIT-World Peace University, Pune.
- **Dr. L. K. Kshirsagar**  
Dean (Engineering), MIT-World Peace University, Pune.
- **Dr. Ramachandra V. Pujeri**  
Director, MIT College of Engineering, Pune.
- **Dr. Anil S. Hiwale**  
Principal, MIT College of Engineering, Pune.
- **Dr. Krishna K. Warhade**  
Dean (Research, Development, Innovation and Consultancy) MIT-WPU, Pune.
- **Dr. Bharat S. Chaudhari**  
Head, MIT-WPU School of Electrical Engineering, Pune.

### CONVENOR

- **Dr. Prasad D. Khandekar**  
Associate Dean (Engineering) and Director Industry Partnerships, MIT-World Peace University, Pune, India.

### CO-CONVENOR

- **Dr. Arti Khaparde**  
Head, MIT-WPU School of Electronics & Communication Engineering, Pune, India.

### COORDINATORS

- **Mr. Rajkumar D. Komati**  
Cell: +91 9822292281  
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- **Mrs. Manisha R. Ingle**  
Cell: +91 9960894948  
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सत्यमेव जयते

An Initiative of

Ministry of Electronics & Information Technology

(MeitY),

Government of India

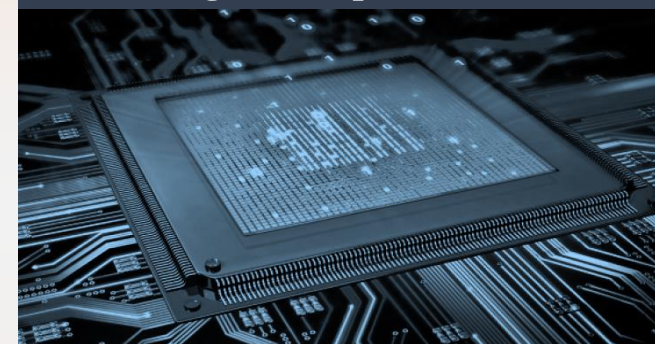


Electronics & ICT Academy

IIT Guwahati, Assam

One-Week Faculty Development Program on

VLSI Design at Deep Submicron Node



from 18 - 22 February, 2019

In Association with

MIT-World Peace University, Pune



Last Date of Registration: 12.02.2019

(Online Registration Link will be open from 10.12.2018)

Venue: MIT-WPU, Kothrud Pune

Registration Fee including GST

Registration Fee (Including Course Material, Snacks and Lunch)

- Rs. 2,950/- for Faculty, Lab Technicians and Project Staff.
- Rs. 1,475/- for ST/SC Faculty.
- Rs. 5900/- for Industry Personnel, Research scholars and Students.

Contact Hours for the Course

40 Hrs (Theory, Hands-on & Tutorial)

## Objective of the Course

This Faculty Development Programme is designed for faculty/professionals/students who want to sharpen their design and development skills. This programme includes extensive hands-on lab emphasizing the demonstration and application of techniques and skills. The Lab includes the following:

- Process technology, Modelling aspects
- Basic Analog blocks
- References
- Digital design flow
- Analog design flow
- Physical Implementation of Design
- Packaging
- Testing and QA issues

## Topics to be Covered

Following Topics will be covered

- Deep Submicron CMOS Technology
- CMOS Analog Circuit Design .
- Introduction To Computer Aided Design (CAD).
- CMOS Switch, Current Mirrors.
- Design of Two-stage Op-Amps .
- Basics of Switched Capacitor Circuits.
- Introduction to Data Converters

## Assignment and Project

1. Assignments will be MCQ based questionnaire.
2. At the end of the course "Project" will be assigned to the participants which would be an application development

## Course Programme

Faculty Development Programme is split into two parts:

- Assignments, Hands On and Project.
- Lectures.

**Mode of Payment: Online Only (RTGS/NEFT)**

### For Online Transfer

**Bank Name: State Bank of India**  
**Account Name: IIT Guwahati R and D E and ICT Academy**  
**Account No.: 36071160089**  
**IFSC Code: SBIN0014262**  
**Bank Name: State Bank of India**  
**Bank Address: IIT Guwahati, GHY- 39.**

## About MIT-WPU Pune

MIT-World Peace University (MIT-WPU) is situated in Pune, the cultural capital of Maharashtra. A landmark in Pune, the architectural grandeur of the buildings of MIT Pune are a testimony to a rich legacy of over 4 decades in the field of education. Known to be among the top education institutions in India it was established in the year 1983 by Prof. Dr. Vishwanath D. Karad and is among the first engineering colleges in the private sector in Maharashtra. MIT World Peace University known as MIT-WPU, is a centre for scientific, educational, technological & spiritual development for the youth. We enable our students to face the challenges of the present and the future technical advancements of a fast changing world by incorporating best of academics, industrial, research and value-based education.

The School of Electronics and Communication Engineering has state of art facilities and highly qualified faculty. The school works with the objective of addressing the critical challenges faced by the industry, society and the academia. With more than 100 faculty and staff members the school offers Bachelor of Technology (B.Tech.) in Electronics and Communication Engineering; Master of Technology (M.Tech.) in Electronics and Communication Engineering with specializations in VLSI and Embedded Systems; Communication Networks and Software, and Doctoral (Ph.D.) programs.

## How to reach



## About E&ICT Academy IIT Guwahati

Electronics and ICT Academy is an initiative of Ministry of Electronics & Information Technology (MeitY), Govt. of India for Faculty/ Research Scholar Development Programme.

Academy has planned short term training programmes on fundamental and advanced topics in IT, Electronics & Communication, Product Design, Manufacturing with hands on training and project work.

Indian Institute of Technology Guwahati, the sixth member of the IIT fraternity, was established in 1994. The academic programme of IIT Guwahati commenced in 1995.



## Contact Details

For more details or any queries please contact  
Project Manager,

E&ICT Academy, IIT Guwahati

Email: [eictacad@iitg.ac.in](mailto:eictacad@iitg.ac.in), [eictacad@gmail.com](mailto:eictacad@gmail.com)

Phone No: +91-3612583182

Mobile No: +91 7086502139

(Working Hours 9:30 am to 5:30 pm)

<https://www.facebook.com/eictacadguwahati>



## How to Apply

**Online** – The participants may log on to the E&ICT Academy IIT Guwahati website: <http://eict.iitg.ac.in/> and fill up the appropriate registration form.

## Venue

MIT-World peace University  
S.No.124, Paud Road, Kothrud,  
Pune, Maharashtra 411038  
Website: <https://mitwpu.edu.in/>