Online Workshop on PHYSICAL DESIGN using Synopsys ICC2 tool

Only for Students currently studying in Indian Universities

In Association with

IIT Guwahati

Physical Implementation Tool-IC Compiler II
Industry Leading Place and Route System
- IC Compiler II is specifically architected to address aggressive performance, power, area (PPA), and time-to-market pressures of leading edge designs.

Workshop Content

- Input collateral needed for design planning and flat PNR in ICC2
- Configure ICC2 scripts for flat design planning and PNR
- Pre-layout timing analysis block using ICC2 internal timing engine
- Configure ICC2 scripts for hierarchical design planning and PNR
- Review ICC2 reference methodologies and project work on ICC2 flat design planning/pnr

Register now
Who can participate?

- Students, Research Scholars, Interested Faculty of engineering/technical institutions in India
- Learn Basics concepts of Physical Design flow and labs before attending this workshop

Course Date:
20th Jan - 24th January 2020 (10am-5pm)

Last date for Registration:
8th January 2020

How to Register
Online – The participants may log on to the E&ICT Academy, IIT Guwahati website and the fill the registration form after payment of the Registration fees.

Online Registration link
http://eict.iitg.ac.in/online_courses_training.html

Registration Fees: Rs 5000/-
- Fees include 5 Days workshop
- 30 days access to Synopsys ICC2
- Non-Refundable

Mode of Payment: Online Only (RTGS/NEFT)
For Online Transfer
Bank Name: State Bank of India
Account Name: IIT Guwahati R and D E and ICT Academy
Account No: 36071160089
IFSC Code: SBIN0014262
Bank Name: State Bank of India
Bank Address: IIT Guwahati, GHY- 39.

Where to get help?
For details, Email, Call or WhatsApp
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